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CONFIRMATION NO. 8716

<b>SERIAL NUMBER</b> 10/501,826	<b>FILING OR 371(c) DATE</b> 07/19/2004 <b>RULE</b>	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2811	<b>ATTORNEY DOCKET NO.</b> IN 020001	
<b>APPLICANTS</b> Pradip Mandal, Bangalore, INDIA;  <b>** CONTINUING DATA *****</b> This application is a 371 of PCT/IB02/05484 12/12/2002  <b>** FOREIGN APPLICATIONS *****</b> EUROPEAN PATENT OFFICE (EPO) 02075258.0 01/22/2002					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged		STATE OR COUNTRY INDIA	SHEETS DRAWING 1	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 1
Examiner's Signature: <i>[Signature]</i> Initials: <i>AT</i>					
<b>ADDRESS</b> 24737					
<b>TITLE</b> Voltage limiting semiconductor pass gate circuit					
<b>FILING FEE RECEIVED</b> 920	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		